

Amendment

The Examiner is respectfully requested to amend the above-identified application as follows.

IN THE CLAIMS:

Please amend Claims 1, 14 and 17 to read as follows. A marked-up copy of Claims 1, 14 and 17, showing the changes made thereto, is attached. Note that all the claims currently pending in this application, including those not presently being amended, have been reproduced below for the Examiner's convenience.

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1. (Twice Amended) An integrated-circuit apparatus comprising:
a CPU and a plurality of circuit blocks to be initialized in accordance with external reset signals, wherein
the circuit blocks each respectively output an initialization completion signal for communicating completion of initialization after the circuit blocks are initialized,
the CPU outputs an enable signal for permitting operations of the circuit blocks in accordance with the initialization completion signals output from the circuit blocks, and
the circuit blocks are permitted to perform the operations by the enable signal and the external reset signals.